

Form PTO-1449 U.S. DEPARTMENT OF
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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

APPLICANT:
David G. Wright et al.

FILING DATE
August 20, 2001

GROUP
2836

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub-Class	Filing Date
A3	4 0 9 9 0 6 8	7/4/1978	Kobayashi et al.	307	200A	
	4 2 4 5 1 5 0	1/13/1981	Driscoll et al.	235	92FP	
	4 3 6 7 4 2 2	1/4/1983	Leslie	307	597	
	4 3 6 7 4 2 3	1/4/1983	Hornung	307	597	
	4 3 9 4 7 0 2	7/19/1983	Boothe	361	92	
	4 4 2 2 1 6 3	12/20/1983	Oldenkamp	365	229	
	4 4 2 8 0 2 0	1/24/1984	Blanchard, Jr.	361	90	
	4 4 2 9 2 3 6	1/31/1984	Nitschke	307	362	
	4 4 3 3 3 9 0	2/21/1984	Carp et al.	364	900	
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	4 4 7 3 4 5 9	9/25/1984	Bose et al.	208	8LE	

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Document No.	Date	Country	Class	Sub-Class	Transl.
					Yes No

OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)

A3	"A Quick Response Peak Detector for Variable Frequency Three-Phase Sinusoidal Signals", By Ching-Tsai Pan et al., IEEE Transactions on Industrial Electronics, Vol. 41, No. 4, August 1994, pp. 434-440.
AB	"Design Considerations for Low-Voltage On-Board DC/DC Modules for Next Generations of Data Processing Circuits", By Michael T. Zhang et al., IEEE Transactions on Power Electronics, Vol. 11, No. 2, March 1996, pp. 328-337.
AB	"Analog Fault Diagnosis Based on Ramping Power Supply Current Signature Clusters", By Shyam S. Somayajula et al., IEEE Transactions on Circuits and Systems - II: Analog and Digital Signal Processing, Vol. 43, No. 10, October 1996, pp. 703-712.

Examiner

A. Berhane

Date Considered

7/03

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AB	4 4 9 3 0 0 0	1/8/1985	Edwards	361	92	
	4 5 8 1 5 5 2	4/8/1986	Womack et al.	307	594	
	4 6 0 7 1 7 8	8/19/1986	Sugie et al.	307	594	
	4 7 8 8 4 5 4	11/29/1988	Tanagawa et al.	307	296R	
	4 7 8 8 4 6 2	11/29/1988	Vesce et al.	307	597	
	4 8 7 9 6 2 3	11/7/1989	Baumgartner et al.	361	33	
	4 9 0 2 9 1 0	2/20/1990	Hsieh	307	296.4	
	5 1 1 1 0 6 7	5/5/1992	Wong et al.	307	272.3	
	5 2 0 7 0 0 6	5/4/1993	Sharpe et al.	33	573	
	5 2 8 0 1 0 8	1/18/1994	Fanning	530	387.7	
	5 3 1 3 1 1 2	5/17/1994	Macks	307	296.3	

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AB	"Design Strategies and Decoupling Techniques for Reducing the Effects of Electrical Interference in Mixed-Mode IC's", By Mark Ingels et al., IEEE Journal of Solid-State Circuits, Vol. 32, No. 7, July 1997, pp. 1136-1141.
AB	"Active Feedback Circuit for Minimization of Voltage Transients During Pulsed Measurements of Semiconductor Devices", By Albert M. Young et al., IEEE Transactions on Instrumentation and Measurement, Vol. 50, No. 1, February 2001, pp. 72-76.
AB	"Low-Voltage Power System Surge Overvoltage Protection", By Dev. Paul, IEEE Transactions on Industry Applications, Vol. 37, No. 1, January/February 2001, pp. 223-229.

Examiner <i>Adolf Berane</i>	Date Considered <i>7/03</i>
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U.S. PATENT DOCUMENTS						
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AB ↓	5 3 4 1 2 6 8	8/23/1994	Ishiguro et al.	361	62	
	5 4 5 7 4 1 4	10/10/1995	Inglis et al.	327	77	
	5 5 4 3 7 4 1	8/6/1996	Purits	327	143	
	5 7 2 3 9 5 8	3/3/1998	Boll et al.	318	432	
	5 7 5 8 0 5 3	5/26/1998	Takeuchi et al.	395	182.02	
	6 1 1 2 1 3 6	8/29/2000	Paul et al.	700	293	
	6 4 3 8 4 6 2	8/20/2002	Hanf et al.	700	297	
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	Document No.	Date	Country	Class	Sub- Class	Transl. Yes No
OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)						
Examiner	<i>Andy Berlane</i>		Date Considered <i>7/03</i>			
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